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27 and 26

Aug 2004

	U	-1	Inventor	Document#	Issue #	Title	Current	Current XN	Retrieval	S	C	P	3	4	Image Doc.	P
1	F	F	Fried, David	US 2004015	2004015	Fin-type resistors	257/536	257/350		F	F	F	F	F	US 2004015	F
2	F	F	Xu, Songlin e	US 2004015	2004015	Methods of forming polymer films and of usin	438/758			F	F	F	F	F	US 2004015	F
3	F	F	Xu, Songlin e	US 2004015	2004015	Methods of using polymer films to form micr	438/694			G	C	C	C	C	US 2004015	F
4	F	F	Lee, Jong-Ho	US 2004015	2004012	Double-gate FinFET device and fabricating m	257/308			F	F	F	F	F	US 2004015	F
5	F	F	Dakshina-Mu	US 2004014	2004011	STRAINED CHANNEL FINFET	257/348			F	F	F	F	F	US 2004015	F
6	F	F	An, Judy Xilin	US 2004014	2004012	Tri-gate and gate around MOSFET devices an	257/270			G	C	C	C	C	US 2004015	F
7	F	F	Grupp, Daniel	US 2004014	2004011	Insulated gate field effect transistor having p	438/197	438/151		F	F	F	F	F	US 2004015	F
8	F	F	Yeo, Yee-Chia	US 2004012	2004011	Semiconductor chip with gate dielectrics fo	257/500			F	F	F	F	F	US 2004015	F
9	F	F	Ahmed, Shibl	US 2004012	2004011	Double gate semiconductor device having se	438/293			F	F	F	F	F	US 2004015	F
10	F	F	Brown, Jeffr	US 2004012	2004011	DRAM cell with enhanced SER immunity	438/257			G	C	C	C	C	US 2004015	F